

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
02008.135001

In Re Application Of: **Masaru DOI et al.**

Serial No.

Filing Date
[Herewith]

Examiner

Group Art Unit

Title: **APPARATUS AND METHOD FOR TESTING SEMICONDUCTOR DEVICE**

Address to:
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37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).

22511

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U.S. PATENT DOCUMENTS				
EXAMINER INITIALS	REF	DOCUMENT NO.	DATE	APPLICANT'S OR PATENTEE'S NAME

FOREIGN PATENT DOCUMENTS						
EXAMINER INITIALS	REF	DOCUMENT NO.	DATE	COUNTRY	TRANSLATION	
					YES	NO
	A1	2-118474	05/02/1990	Japan		X
	A2	2000-149593	05/30/2000	Japan		X
	A3	2000-162290	06/16/2000	Japan		X
	A4	9-97196	04/08/1997	Japan		X
	A5	2001-201532	07/27/2001	Japan		X
	A6	2002-25294	01/25/2002	Japan		X
	A7	2002-196053	07/10/2002	Japan		x

OTHER DOCUMENTS		
EXAMINER INITIALS	REF	(Include Author, Title, Date, Pertinent Pages, etc.)
	A8	Patent Abstracts of Japan, Publication No. 02-118474 dated May 2, 1990, 2 pgs.

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	B1	Patent Abstracts of Japan, Publication No. 2000-149593 dated May 30, 2000, 2 pgs.
	B2	Patent Abstracts of Japan, Publication No. 2000-162290 dated June 16, 2000, 2 pgs.
	B3	Patent Abstracts of Japan, Publication No. 09-097196 dated April 8, 1997, 2 pgs.
	B4	Patent Abstracts of Japan, Publication No. 2001-201532 dated July 27, 2001, 2 pgs.
	B5	Patent Abstracts of Japan, Publication No. 2002-025294 dated January 25, 2002, 2 pgs.
	B6	Patent Abstracts of Japan, Publication No. 2002-196053 dated July 10, 2002, 2 pgs.
	B7	International Search Report issued in Int'l Application No. PCT/JP02/05924 mailed on September 24, 2002, 3 pgs.

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CERTIFICATE OF MAILING BY "EXPRESS MAIL" (37 CFR 1.10) Applicant(s): Masaru DOI et al.			Docket No. 02008.135001
Serial No.	Filing Date [Herewith]	Examiner	Group Art Unit

Invention:

APPARATUS AND METHOD FOR TESTING SEMICONDUCTOR DEVICE

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